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<b>SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(Use as many sheets as necessary)</i>				Application Number	10/590,876
Sheet	1	of	1	Filing Date	July 10, 2007
				First Named Inventor	Hartmut SCHUMACHER
				Art Unit	2836
				Examiner Name	Michael Rutland Wallis
				Attorney Docket Number	10191/4350

U.S. PATENT DOCUMENTS					
Examiner Initials *	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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FOREIGN PATENT DOCUMENTS					
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		Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (if known)			
/M.R.W./		DE 35 06 487	September 4, 1986		*
/M.R.W./		WO 03/059681	July 24, 2003		*

\* English language abstract provided.

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/M.R.W./		Tietze: Semiconductor Switching Technology, 12 <sup>th</sup> Edition, Springer-Publishing 2002, ISBN 3-540-42849-6, pages 947, 957-960, particularly 959**			

\*\* English language translation of page 959 provided herewith.

Examiner Signature	/Michael Rutland Wallis/	Date Considered	08/05/2011
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